



10/633,247

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PATENT

I hereby certify that on the date specified below, this correspondence is being deposited with the United States Postal Service as first-class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

May 10, 2005
DateAlexandra Beggs
Alexandra Beggs**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant : Theodore T. Pekny Attorney Docket No.: 500118.02
Patent No. : US 6,754,111 B2 Serial No. : 10/633,247
Issue Date : June 22, 2004 Filed : August 1, 2003
Title : DUAL BANDGAP VOLTAGE REFERENCE SYSTEM AND METHOD FOR
REDUCING CURRENT CONSUMPTION DURING A STANDBY MODE OF
OPERATION AND FOR PROVIDING REFERENCE STABILITY DURING AN
ACTIVE MODE OF OPERATION

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Certificate
MAY 18 2005
of Correction

Sir:

A Certificate of Correction under 35 U.S.C. § 254 is respectfully requested for the above-identified patent in order to correct Patent and Trademark Office errors made during the printing of the patent. The changes in the patent needed to correct the errors are as follows:

<u>Column, Line</u>	<u>Reads</u>	<u>Should Read</u>
Column 3, Line 26	"providing a highly stable"	--providing highly stable--
Column 4, Lines 60 and 62	"CEI#signal"	--CEI# signal--
Column 4, Line 64	"with a substantially no"	--with substantially no--

MAY 23 2005

Column 5, Line 13	"standby bandgap, voltage"	--standby bandgap voltage--
Column 5, Line 39	"the band gap switching"	--the bandgap switching--
Column 6, Line 11	"operated during the standby"	--operates during the standby--
Column 6, Line 21	"reference 304, to"	--reference 304 to--
Column 6, Lines 32 and 36	"WL1-N using the"	--WL1-WLN using the--
Column 6, Line 44	"generating about VX voltage"	--generating VX voltage--
Column 7, Line 3	"accessing row of memory"	--accessing rows of memory--
Column 7, Line 16	"transfer operation"	--transfer operations--
Column 7, Lines 56- 57	"standby band gap voltage"	--standby bandgap voltage--
Column 8, Line 31	"and OE#in combination"	--and OE# in combination--
Column 8, Line 41	"404 develops applies signals"	--404 applies signals--
Column 9, Line 3	"440a, 44b to access"	--440a, 440b to access--
Column 10, Line 30	"a digital cameras,"	--digital cameras,--
Column 14, Line 13	"having desired value;"	--having a desired value;--

The above errors for which correction is requested under 35 U.S.C. § 254 were made in the printing of the patent. The errors are considered sufficiently important to justify the processing of a Certificate of Correction under 35 U.S.C. § 254. A Form PTO-1050, in duplicate, is enclosed herewith.

The Commissioner is hereby authorized to charge payment of any fees associated with this communication to Deposit Account No. 50-1266. A duplicate copy of this sheet is enclosed.

Favorable consideration of this Request is respectfully requested.

Respectfully submitted,

Date:

May 10, 2005

By:

Edward W. Bulchis

Edward W. Bulchis, Reg. No. 26,847

Customer No. 27,076

Dorsey & Whitney LLP

1420 Fifth Avenue, Suite 3400

Seattle, WA 98101

(206) 903-8785

Attorney for Applicant(s)

EWB:tdk

Enclosures:

Postcard

Form PTO-1050 (+ copy)

500118.02 req cert correct



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Alexandra Beggs
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Attorney for Applicant(s)

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Enclosures:

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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

PATENT NO. : US 6,754,111 B2
DATED : June 22, 2004
INVENTOR(S) : Theodore T. Pekny

It is certified that errors appear in the above identified patent and that said Letters Patent is hereby corrected as shown below:

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MAILING ADDRESS OF SENDER:

DORSEY & WHITNEY LLP
1420 Fifth Avenue, Suite 3400
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Patent No. US 6,754,111 B2

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FORM PTO-1050 (REV. 3-82)

500118.02 PTO 1050

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